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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Guy B. Irving et al.
Serial No.: 09/848,816
Filing Date: May 4, 2001
Group Art Unit: 2111
Examiner: Khanh Nmn Dang
Title: **SERVER CHASSIS HARDWARE MASTER
SYSTEM AND METHOD**

MAIL STOP AMENDMENT

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

DECLARATION PURSUANT TO 37 C.F.R. § 1.131

I, the undersigned, hereby declare and state that:

1. I am over the age of 21 years, of sound mind, and competent in all respects to make this Declaration.

2. I am an inventor of the subject matter of the above-referenced patent application, entitled *Server Chassis Hardware Master System and Method*, filed on May 4, 2001 (the "Application").

3. Guy B. Irving and I (the "Inventors") conceived the subject matter of at least Claims 1, 9, 10, 13, and 16 of the Application (the "Invention") prior to July 12, 2000.

4. I participated in the design and construction of a prototype that incorporated the subject matter of the Invention. Attached as Exhibit A is the sixth version of a design schematic that was used to construct the prototype.

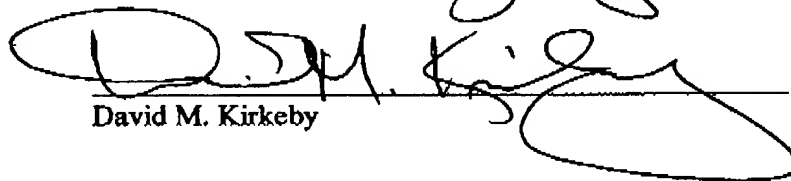
5. During a time period beginning prior to July 12, 2000, and ending upon completion of the prototype, the Inventors worked diligently toward the design and construction of the prototype.

6. I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true. Further, I declare that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the Application or any patent issuing thereon.

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Declaration pursuant to 37 C.F.R. § 1.131 in regard to 09/848,816.

Signed this 19 day of July, 2004.


David M. Kirkeby

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20 Fiducials, Holes, Spare Gates

Released Rev. A.6 (000723.2000)



Anigma Inc.

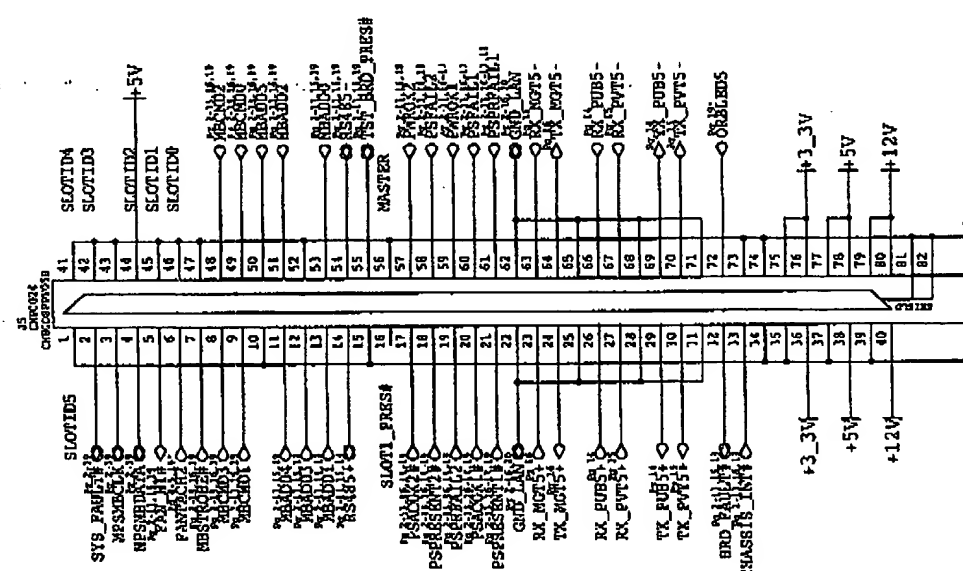
Title: RLXHP

Drawing Number: RLXHP-PCB-01-SC-Revision: A.6

Release Date: July 23, 2000

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address=000101b

91015

Address=0001000b

SLOTS

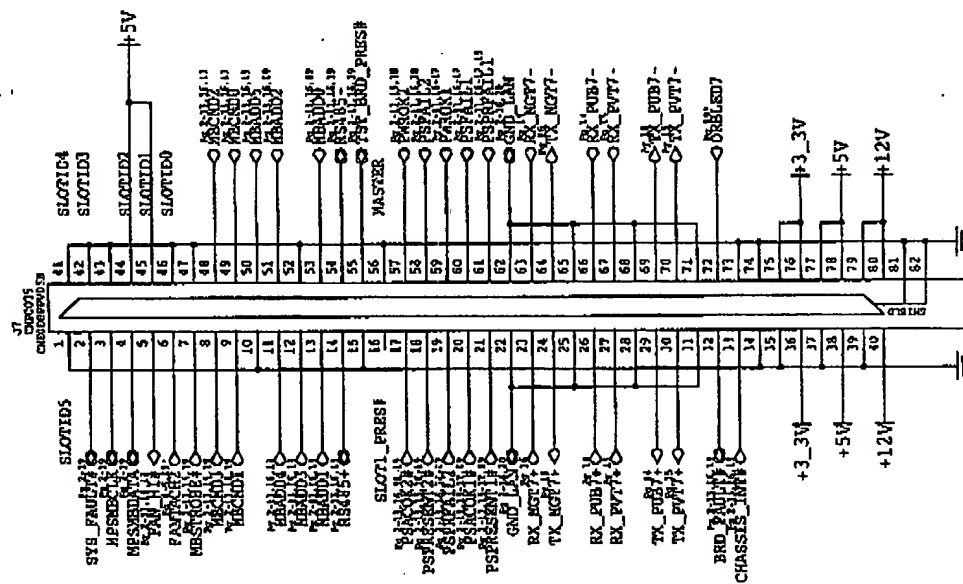


Anigma

Title: ALXMP

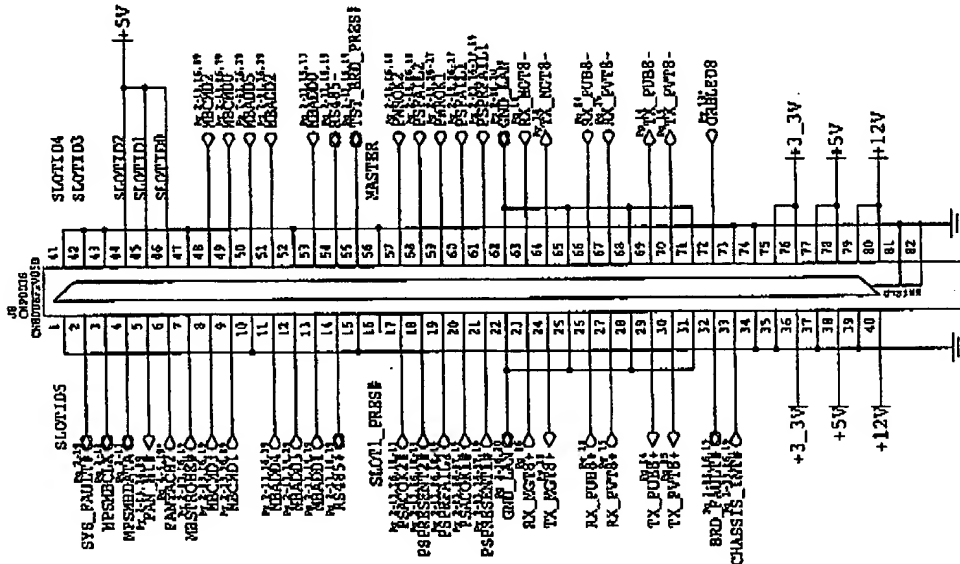
Drawing Number: SLXNP-PCB-07-SCHREVISION:A.6

Release Date: July 23, 2000	Sheet 4 of 20
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Address=000110b

SLOT7



Address=000111b

SLOT8



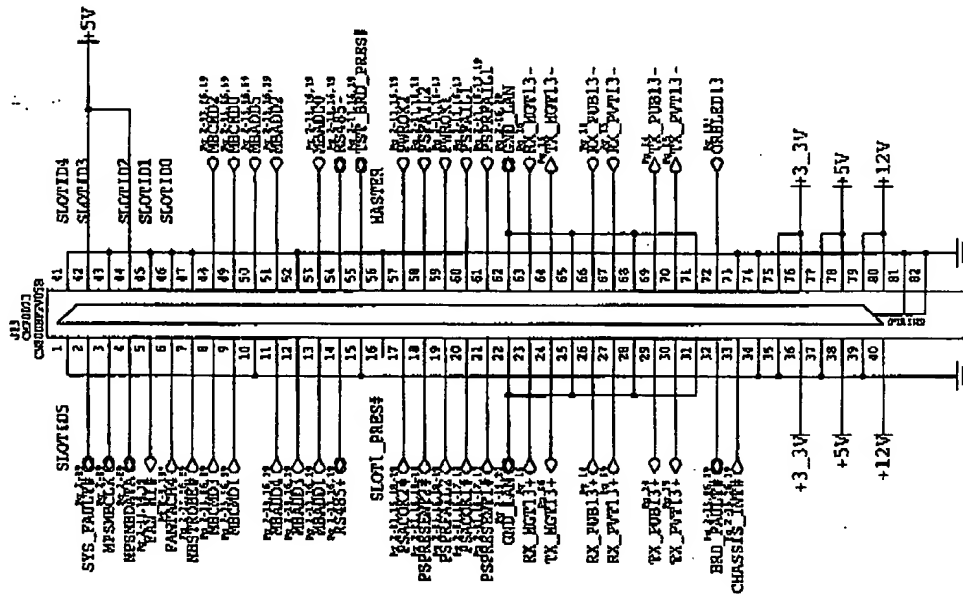
Anigma
Inc.

Title: ELXNP

Drawing Number: RUXNP-PCB-01-SC (Revision) A.5

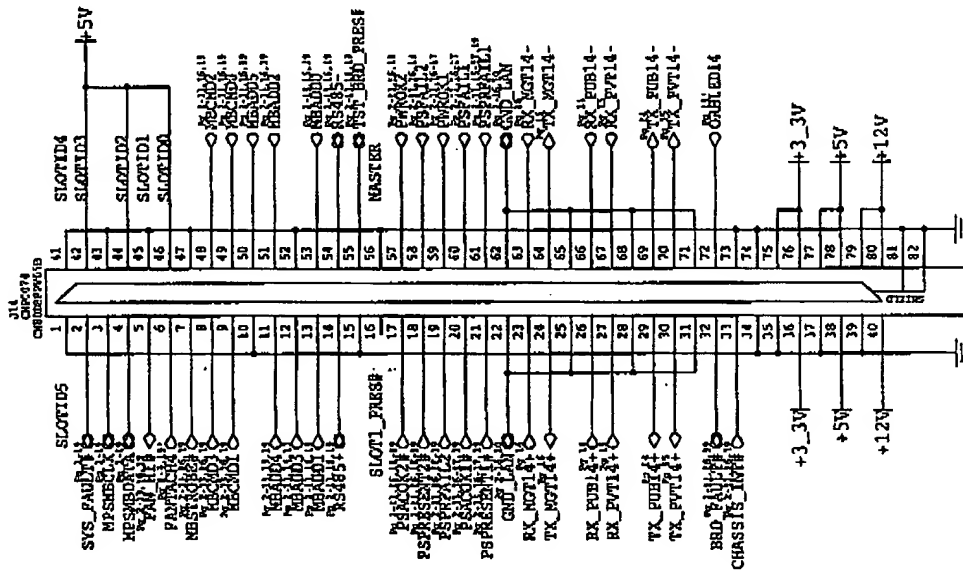
Release Date: July 23, 2000 Sheet 5 of 20





Address=001100b

SLOT13



Address=001101b

SLOT14
DEBUG SLOT



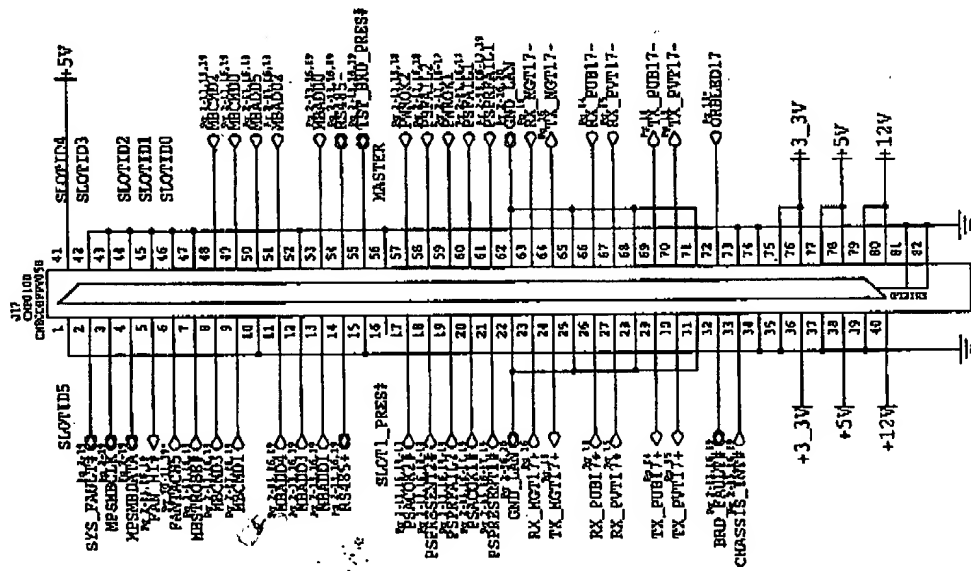
Anigma
Inc.

Title: RLAMP

Drawing Number: RLAMP-PCB-01-SCH Revision: A.6

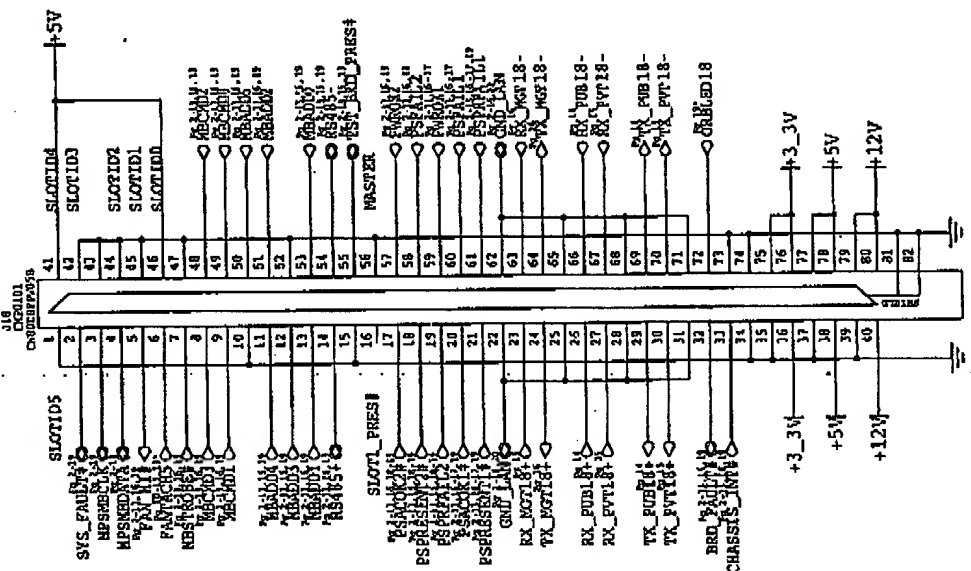
Release Date: July 23, 2000 Sheet 8 of 20





Address=010000b

SLOT17



Address=010001b

SLOT18



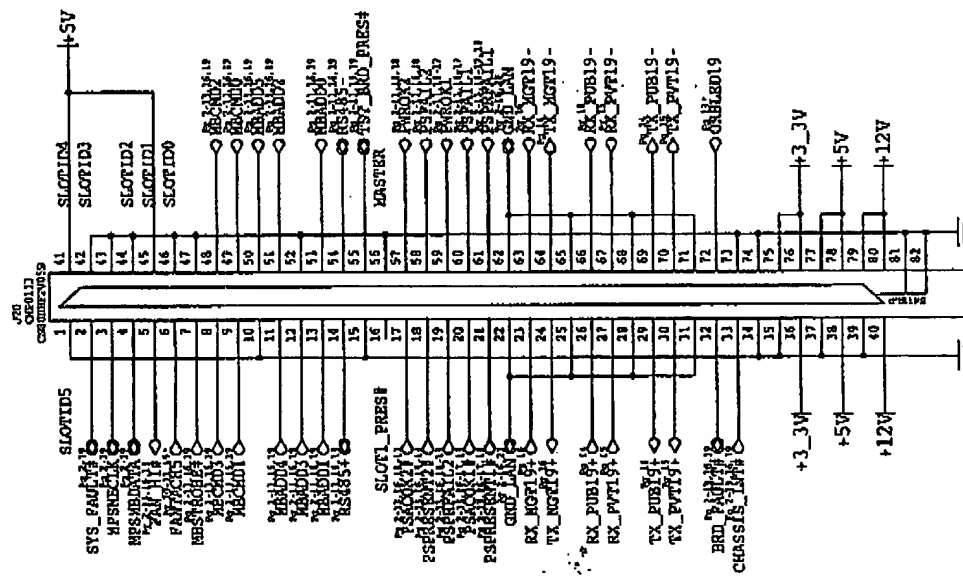
Anigma
Inc.

Title: RLXNP

Drawing Number: RLXNP-PCB-01-SC/Revision: A.6

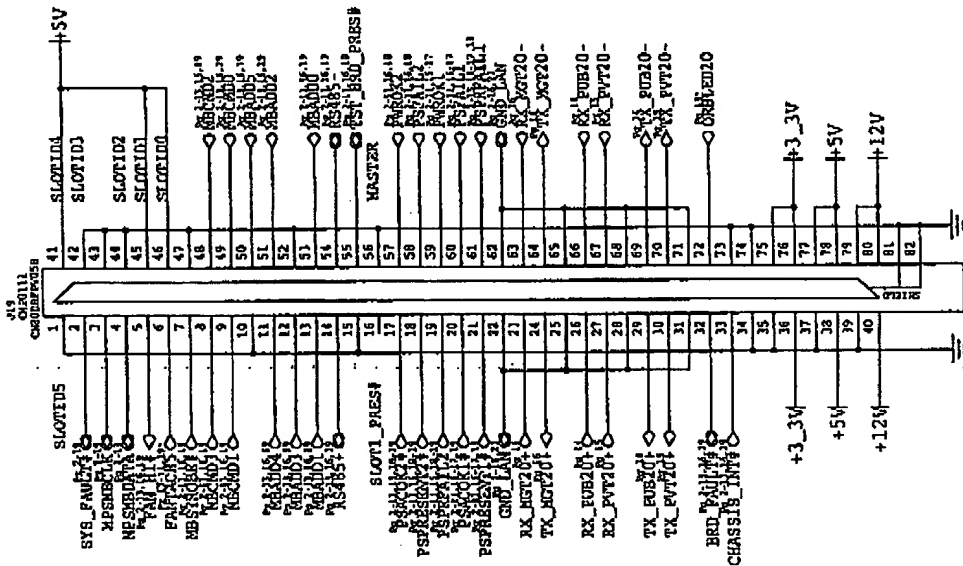
Release Date: July 23, 2000

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Address=010010b

SLOT19



Address=010011b

SLOT20



Anigma

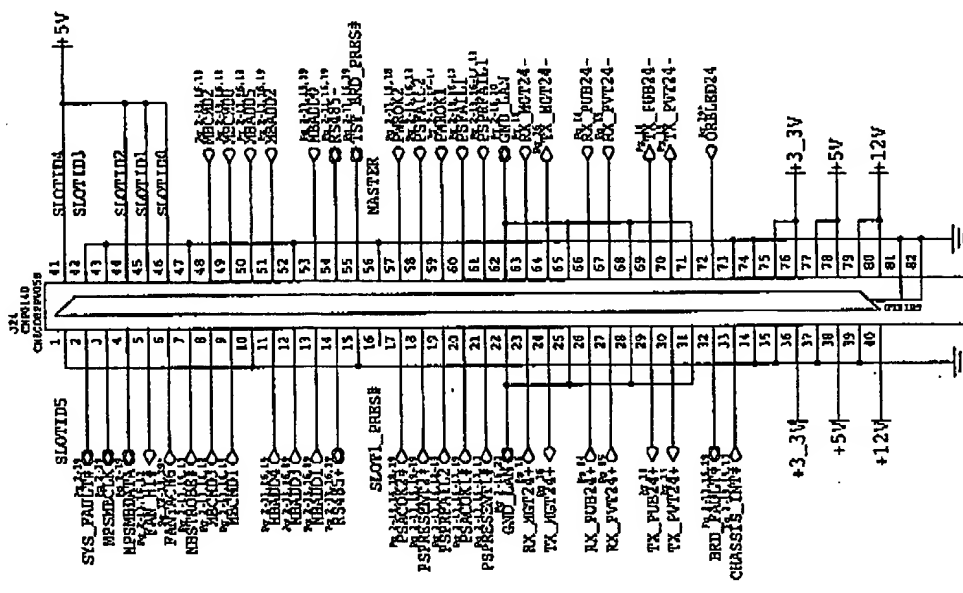
Title: RUXXP

Drawing Number: RUXXP-PCB-01-SC|Revision: A.6

Release Date: July 21, 2000

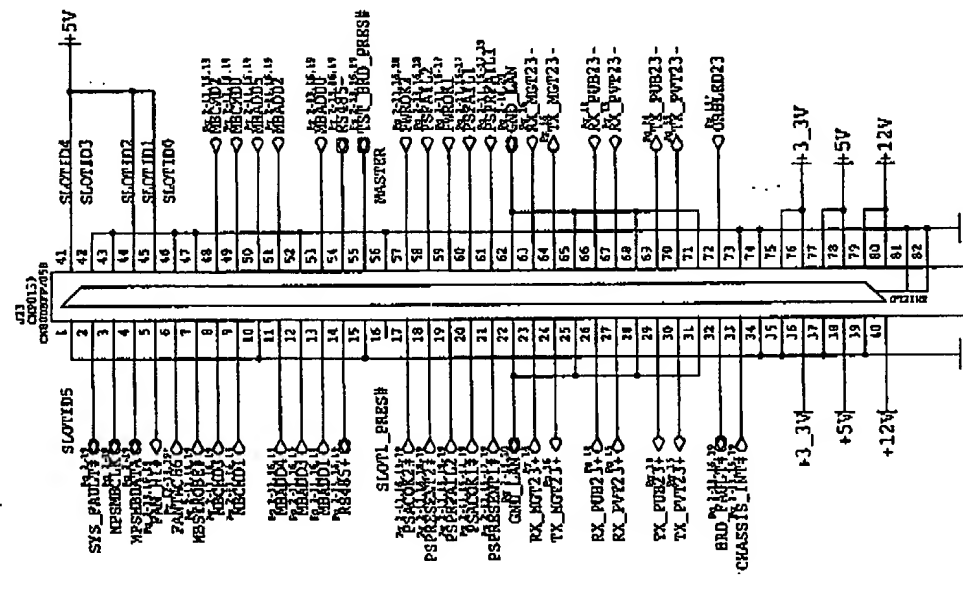
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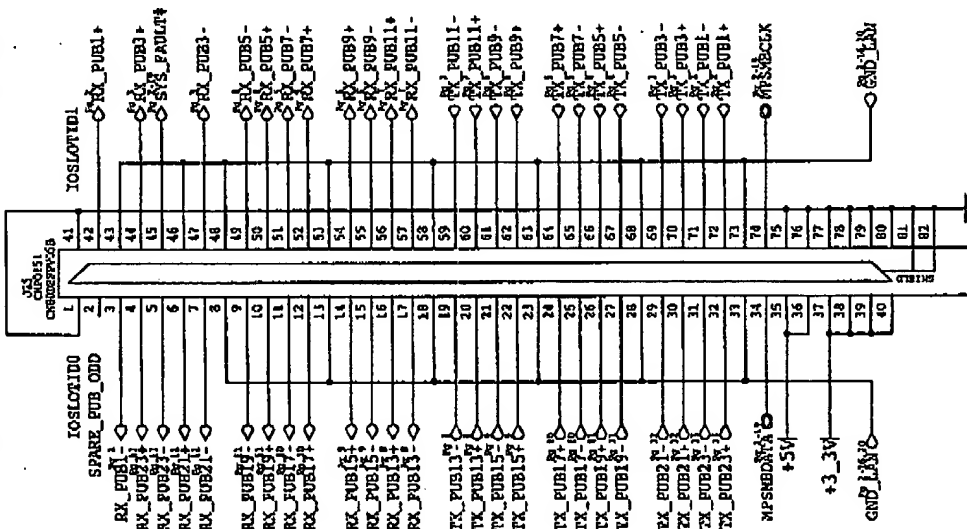
Address=010110b

SLOT24



Address=010110b

SLOT23



PUBLIC LAN - EVEN SLOTS

RLXRJ21 SLOT 2

Address=01b

PUBLIC LAN - ODD SLOTS

RLXAJ21 SLOT 1

Address=00b

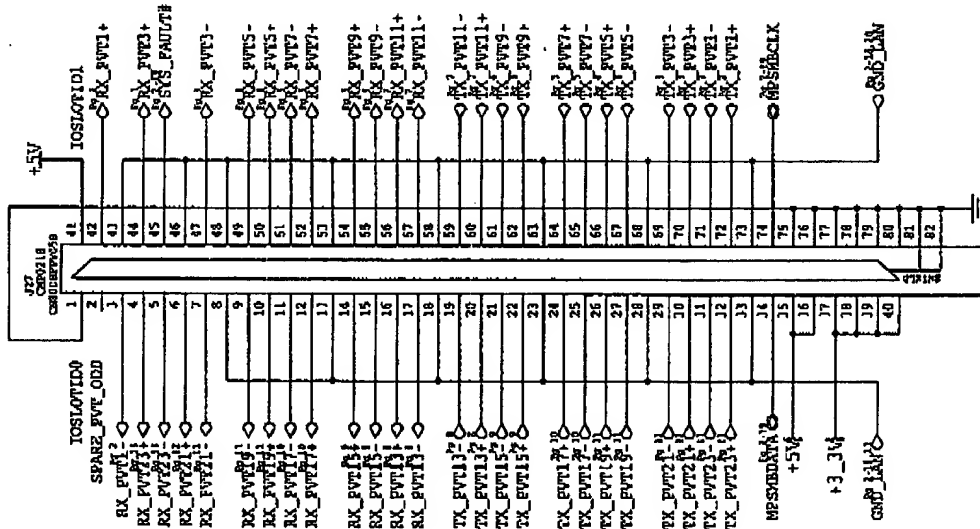


Anigma

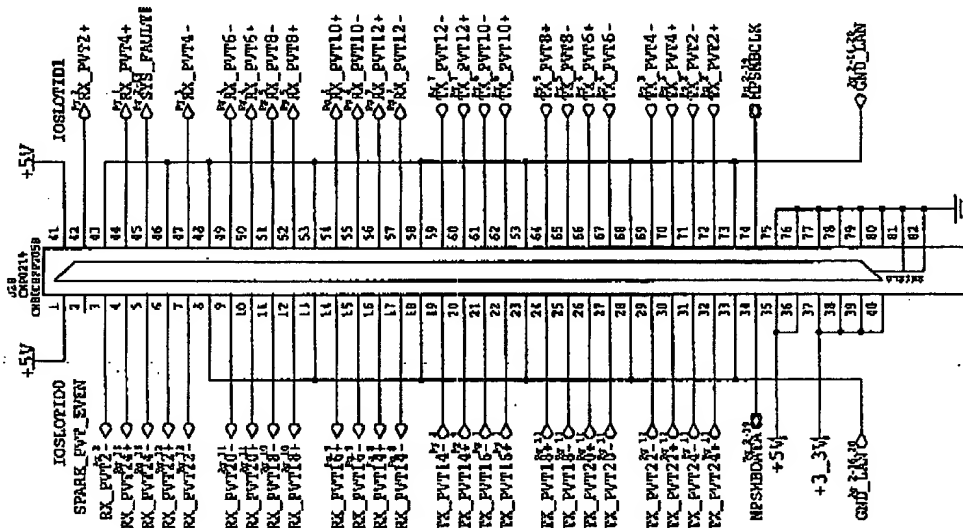
Title: RLYMP

Drawing Number: ALXNP-PCB-01-SCH Revision: A.6

Release Date: July 23, 2000	Sheet 14 of 20
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PRIVATE LAN - ODD SLOTS
RLXRJ21 SLOT 3
Address=10b

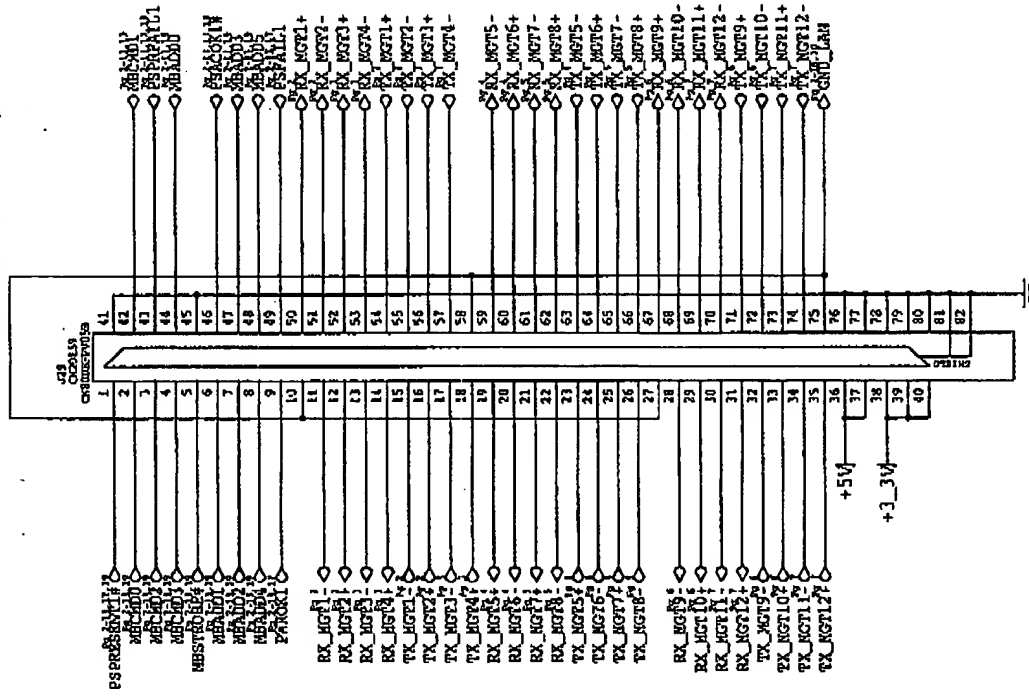


PRIVATE LAN - EVEN SLOTS
RLXRJ21 SLOT 4
Address=11b

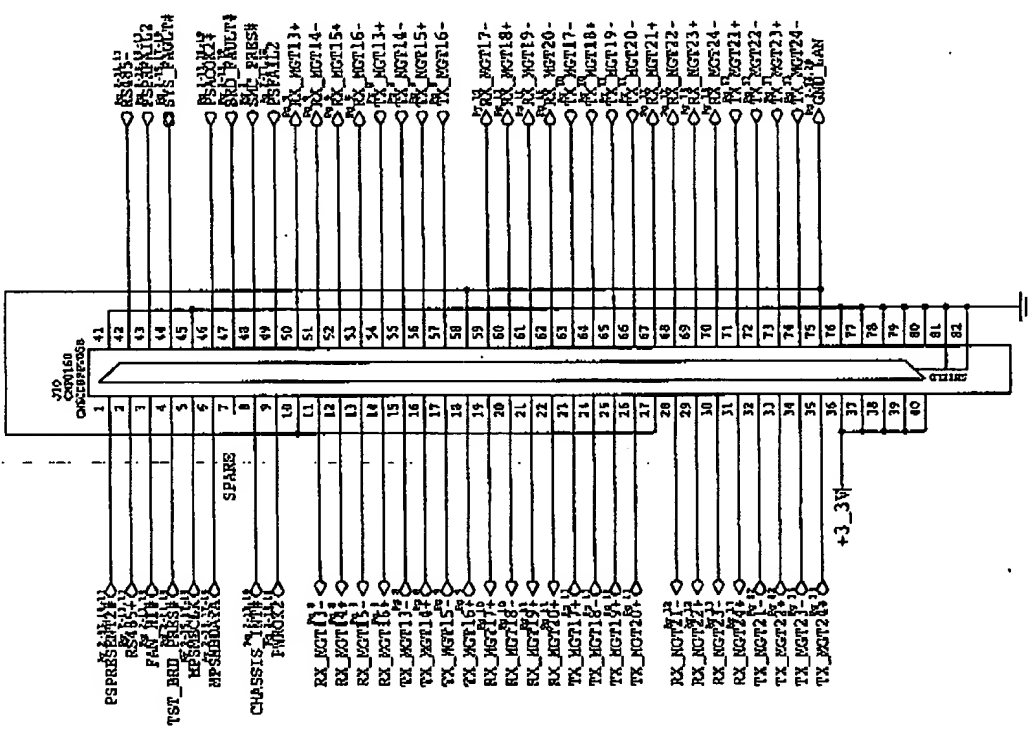


Anigma
inc.

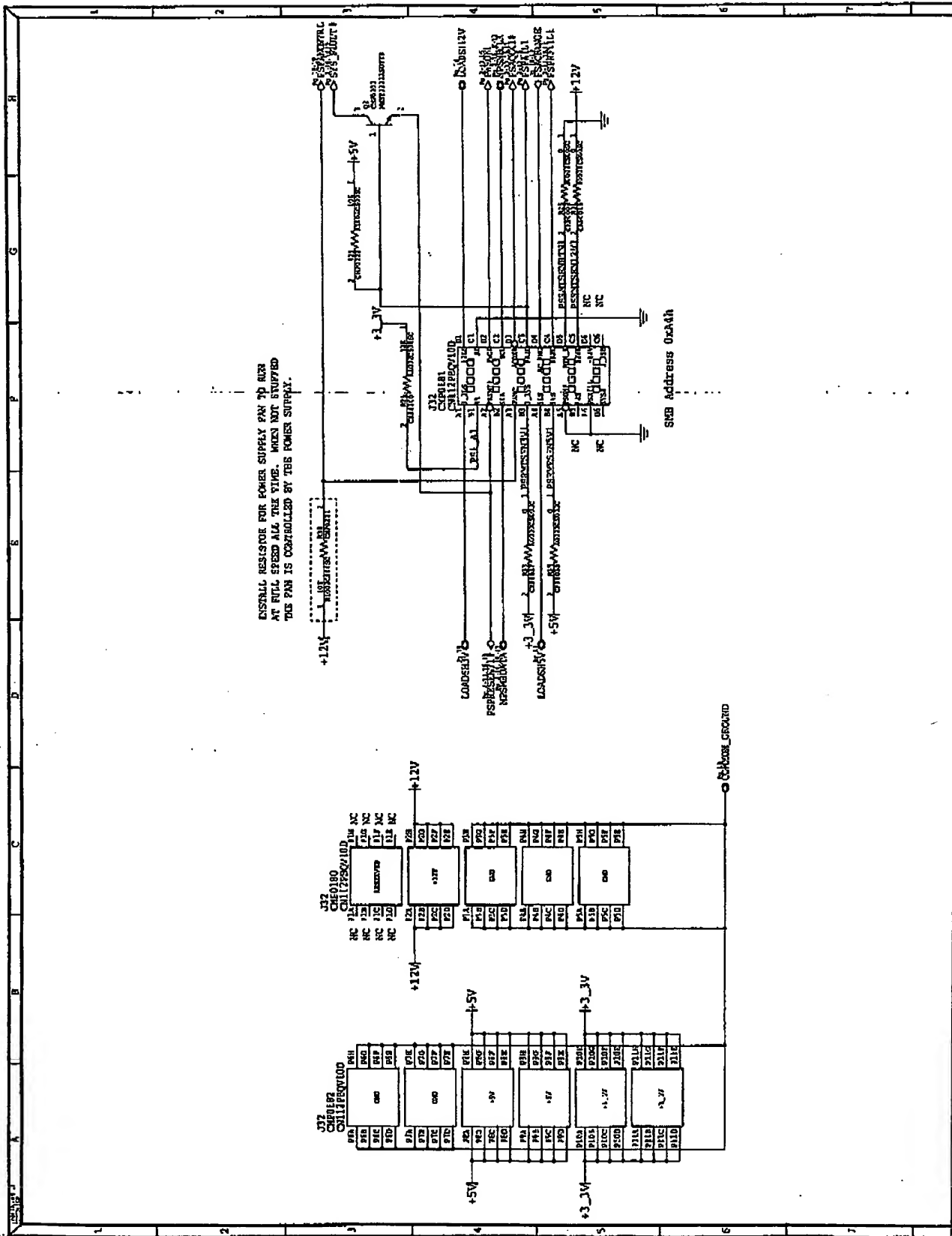
Title: RLXJP
Drawing Number: RLXJP-PCB-01-SC Revision: A.6
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HUB CONNECTOR 1



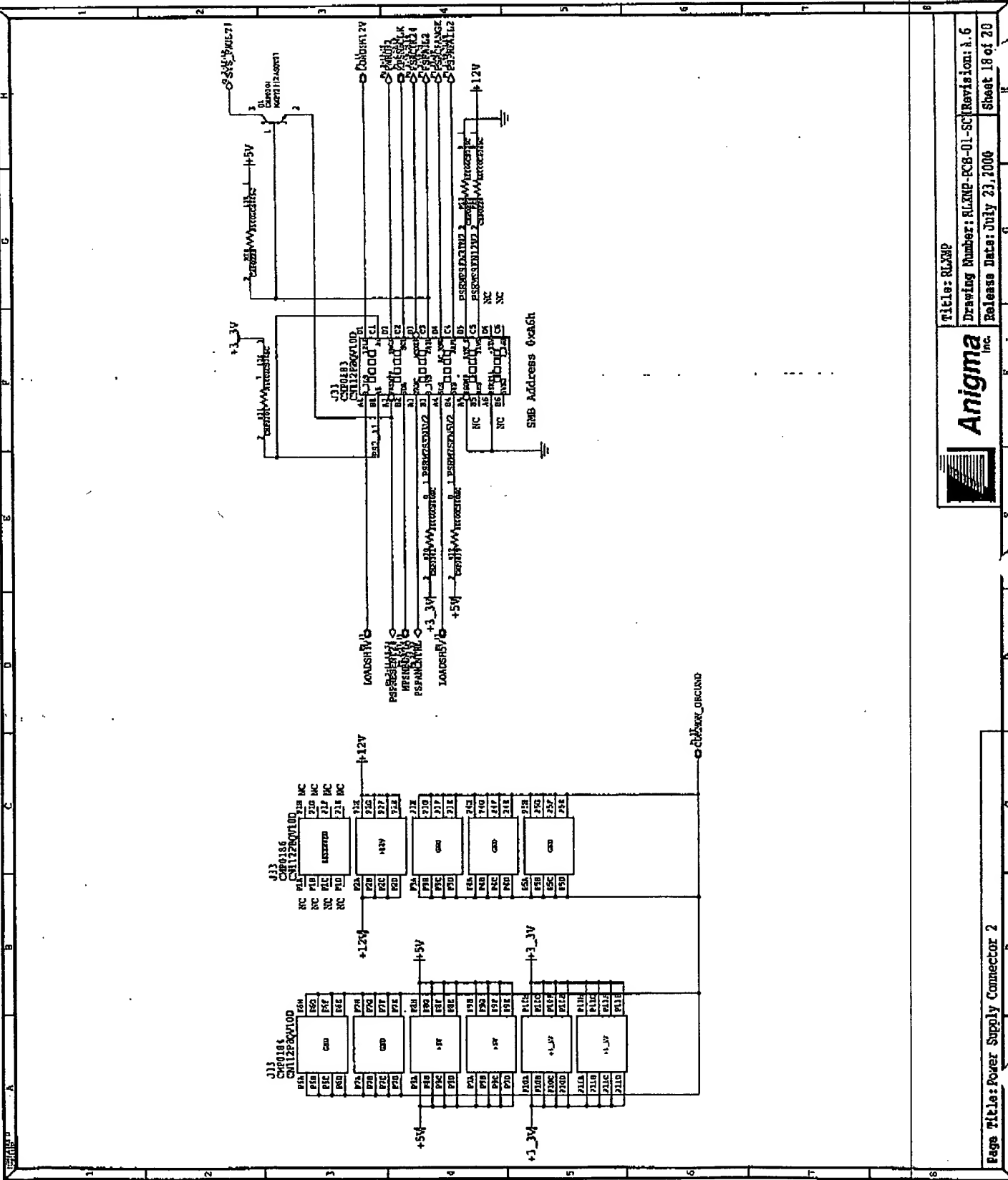
HUB CONNECTOR 2



Anigma
INC.

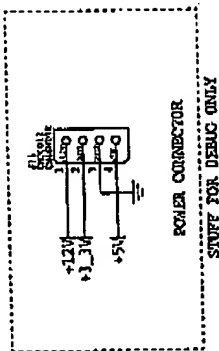
File: RLXNP

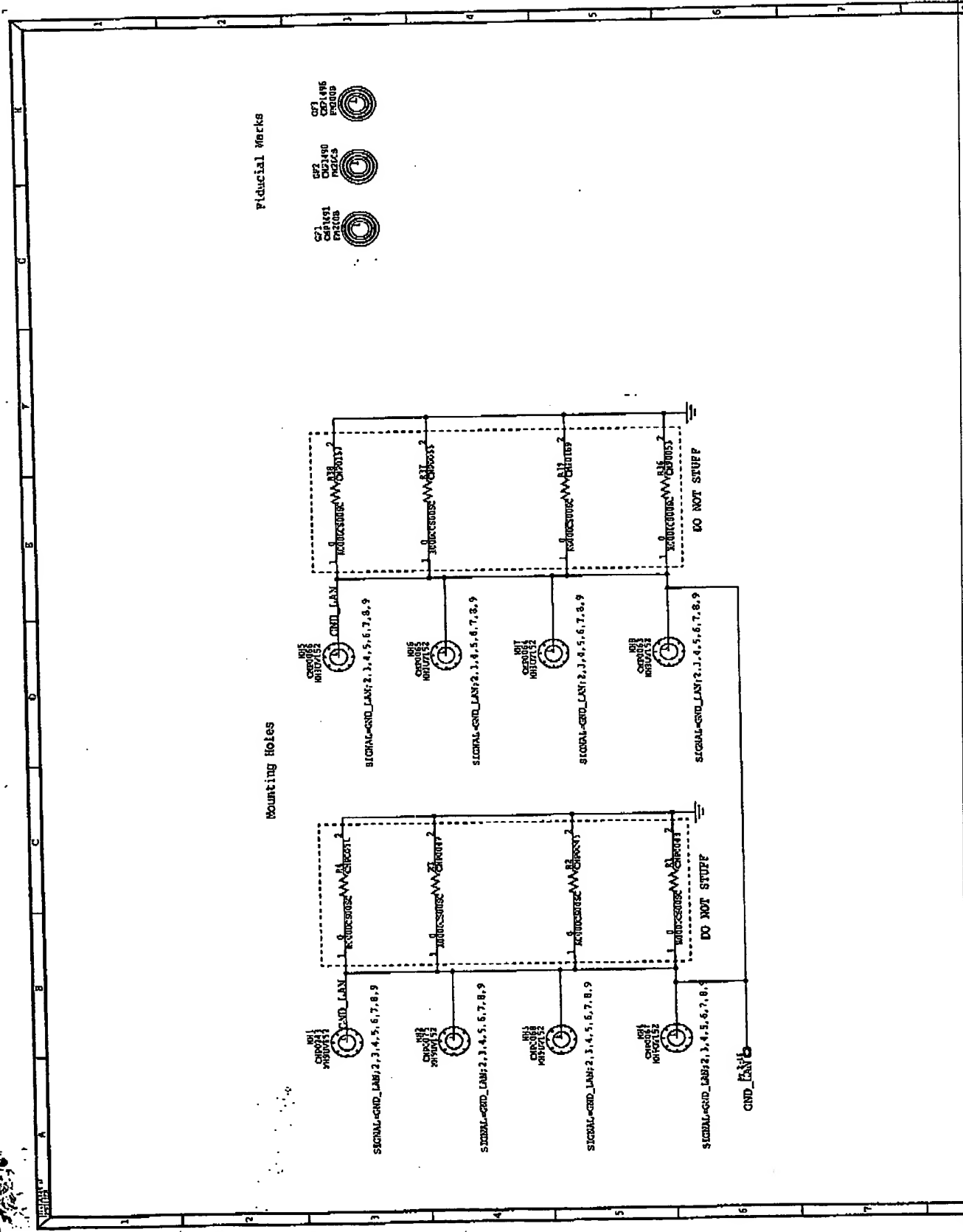
Drawing Number: RLXNP-PCB-01-SC-Revision A.6
Release Date: July 23, 2000
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Title: RLXMP
 Drawing Number: RLXMP-PCB-01-SC [Revision: 1.6]
 Release Date: July 23, 2006
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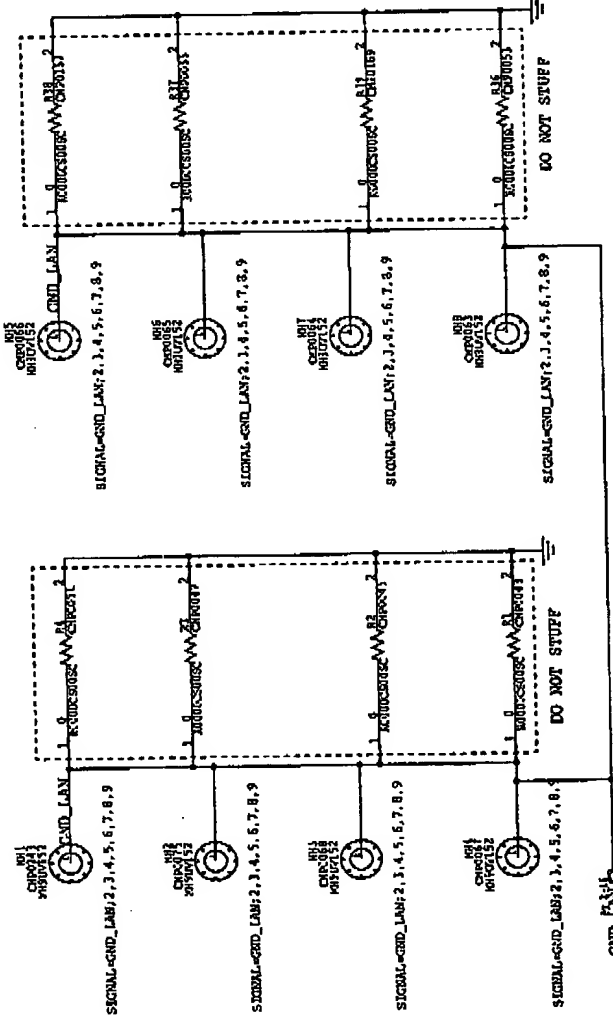




Fiducial Marks



Mounting Holes



Title: REXMP
 Drawing Number: REXMP-PCB-01-SC Revision: A.6
 Release Date: July 23, 2000 Sheet 20 of 20

